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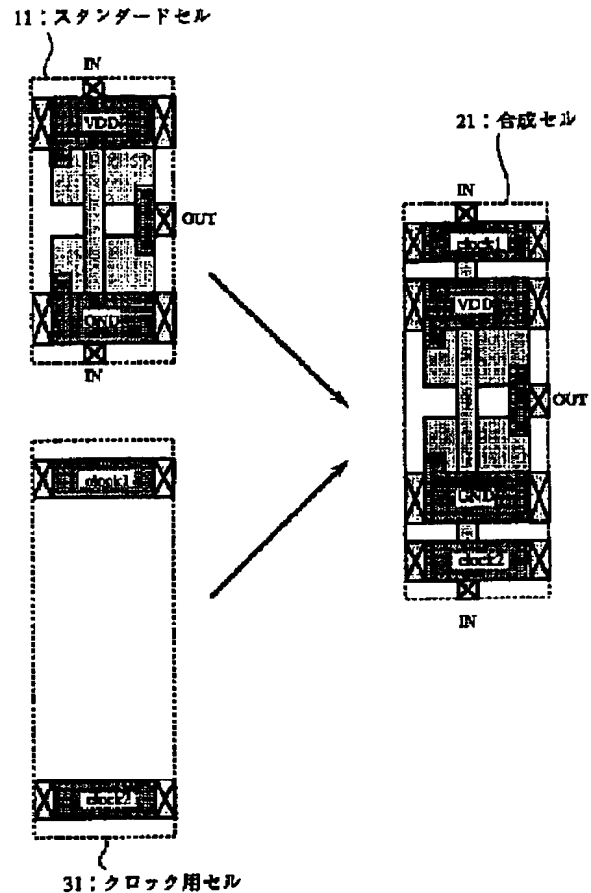
APPLICATION DATE : 08-01-93
APPLICATION NUMBER : 05018083

APPLICANT : MAZDA MOTOR CORP;

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TITLE : DESIGNING DEVICE FOR
SEMICONDUCTOR INTEGRATED
CIRCUIT



ABSTRACT : PURPOSE: To provide a semiconductor integrated circuit designing device which can automatically wire clock lines in a state where the wiring can cope with an increase in operating speed.

CONSTITUTION: The layout pattern information of standard cells which become constituents of semiconductor integrated circuits are prepared as the library of standard cells 11. On the other hand, a cell 31 for clock having the layout pattern information of clock lines is separately prepared. An automatic arranging and wiring means extracts one required standard cell 11 from the cells 11 in the library based on given circuit diagram information and prepares a synthesized cell 21 by superimposing the cell 31 for clock upon the extracted cell 11. Then the automatic arranging and wiring means prepares the layout pattern corresponding to the given circuit diagram information by two- dimensionally arranging the synthesized cell 21.

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